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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 10/068,988 02/11/2002 PAT 6,653,211

** FOREIGN APPLICATIONS *****

JAPAN 033808/2001 02/09/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 01/15/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	JAPAN	DRAWING 3	4	1
Verified and Acknowledged	Examiner's Signature Initials				

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TITLE

Semiconductor substrate, SOI substrate and manufacturing method therefor

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